

F6002 Special Logic Control Card

This card is designed to produce and manage functional logics through flip/flop, toggle and timer devices. Timers can be configured to operate in two modes (A and B).



Main Characteristics

- Operating mode configurable through ProHS81 software program
- Up to 100 software logic devices for flip/flop functions of a set/reset type
- Up to 100 software logic devices for “toggle” functions
- Up to 100 software timers individually programmable from 0 to 255 seconds
- Can be hot-swapped
- Communication management by FPGA
- Internal logic management by micro-controller
- SMD technology multilayer circuit
- Front plug-in on 19" rack, with locking screws

Operation

The card deploys the virtual inputs constituted by the output variables from the other panel cards to produce virtual outputs, which are obtained through software elaboration by internal logic devices.

The virtual outputs thus generated are input variables to other cards and are used to create particular functional logics.

Status Indication

LED	Function
F1	Lights up when an input variable to the card is activated
F2	Lights up when an output variable from the card is activated
F3	Blinks when timers are activated
DISABLED	Lights up when an input or an output variable to/from the card is disabled

Configuration

Both the logic devices and the input/output variables are defined during configuration, which is carried out through the ProHS81 program.

The logic devices that can be produced by means of this card are the following:

Software Device Configuration				
Type	Variables	Mode	Alarm Delay	Alarm Signaling
Flip Flop	Input/Output	Set/Reset	0-255 seconds	Normal/Silent/Buzzer only
Toggle	Input/Output	In-Out	Not applicable	Normal/Silent/Buzzer only
Timer	Input/Output	Timer A or Timer B	0-255 seconds	Normal/Silent/Buzzer only

Terminal Block Connection

This card has no connection with the field and, therefore, needs no wiring to a terminal block.

Logic Device with Flip-Flop Function - Set/Reset Type	
<p>A Set input provides a high output (Q), after a delay time (T).</p> <p>The output is zeroed by the front end of the Reset input.</p> <p>If Reset occurs within the delay time (T), the output (Q) remains low.</p>	
Logic Device with Timer Function - Mode A	
<p>The input (In) must remain high, during the set delay time (T), otherwise the output (Out) remains low.</p> <p>After the delay time (T), the output (Out) remains high as long as the input (In) is also high.</p>	
Logic Device with Timer Function - Mode B	
<p>The activation of the input (In) increases the output (Out), which remains high for the duration of the set delay time (T), or as long as the input (In) remains active.</p>	
Logic Device with Toggle Function	
<p>The activation (on front end) of the input (In) raises the output (Out).</p> <p>The next activation of the input (In) zeros the output (Out).</p>	